

Edited book on

VLSI and Hardware Implementation using Machine Learning Methods

The published book will be submitted for WoS and SCOPUS indexing.

About the Book:

This book aims to provide the latest machine learning based methods, algorithms, architectures, and frameworks designed for VLSI design. The book will contain chapters on case studies as well as novel research ideas mentioned in the scope of the book.

Important Dates

Abstract Submission: 10-11-2020

Abstract Acceptance: 15-11-2020

Full Chapter Submission: 15-12-2020

Revision Notification: 31-12-2020

Final submission: 25-01-2021

How to Submit:

Scientists, researchers, academicians, research scholars and others working in the related fields can submit the abstract by writing to us at **Sandeep.saini@lnmiit.ac.in** and **Kusum@lnmiit.ac.in**

Book Editors:

1. Sandeep Saini, LNMIIT Jaipur, India
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3. Prof. G R Sinha, MIIT Mandalay, Myanmar

Scope of the book (Not limited to):

- Modern ML methods for VLSI applications
- VLSI Implementation of Deep Neural Network
- Spike-driven synaptic plasticity theory, simulation and VLSI implementation
- ML methods for Hardware Security
- ML approaches for FPGA implementation
- Image Processing with FPGA implementation
- Hardware based Sign Language Recognition
- ML implementation on FPGA using partial reconfiguration
- Hardware based framework for accelerating statistical machine learning
- SRAM computation-in-memory macro for multiple-bit CNN-based machine learning
- ML methods for IC testing
- ML approaches for yield management in semiconductor manufacturing
- ML systems for intelligent services in the IoT
- ML methods for hardware performance optimization
- System on Chip design using ML
- **Other related topics**

There are no submission/processing charges for the publication