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The conference covers all broad disciplines of Electronic, Electrical and Computer Science Engineering. The organizing committee of the conference invites full papers submission from researchers, academicians, scientists and industrialists. Paper must be an original contribution and should not have been presented, submitted and published anywhere else. Authors are encouraged to submit their papers under tracks Track 1: Intelligent Wireless Systems, Track 2: High-Speed Electronic Devices, Track 3: Artificial Intelligence and Machine Learning and Track 4: Advanced Power and Energy Systems but are not limited to. For more details, authors are requested to kindly visit the conference website: <https://siet.in/icissi2022/>

ABOUT THE CONFERENCE

International Conference on Intelligent Systems and Smart Infrastructure is being jointly organised by Shambhunath Institute of Engineering and Technology, Prayagraj U.P INDIA, Institute of Engineering and Technology (IET) Lucknow, U.P INDIA and Manipal University Jaipur, Rajasthan INDIA with an aim to provide a platform for researchers, scientists, technocrats, academicians and engineers to exchange their innovative ideas and new challenges being faced in the field of emerging technologies. This conference will be a platform to focus on the core technological developments in the emerging fields like machine learning, intelligent systems, smart infrastructure, advanced power technology etc.



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**21-22
May 2022
S.I.E.T, Prayagraj**



ICISSI 2022

International Conference
on Intelligent Systems
& Smart Infrastructure



PAPER SUBMISSION

Papers must be of original contributions and should not have been presented or published anywhere. The authors are encouraged to submit their paper through easy chair link:

<https://easychair.org/conferences/?conf=icissi2022>

Publishing and Indexing Information: All the submitted papers for ICISSI-2022 will undergo the review process by the Review Committee. The accepted and presented papers during the conference will be published in the proceedings of 1st International Conference on Intelligent Systems and Smart Infrastructure (ICISSI-2022). The proceeding of ICISSI-2022 will be published by CRC Press, Taylor & Francis Group. The proceeding will be submitted to SCOPUS and Google Scholar by CRC Press, Taylor & Francis Group. The length of the manuscript should not be exceeded more than 6 pages. In special case, authors are allowed to submit the manuscript more than 6 pages with extra charge of Rs. 500 per page.

REGISTRATION FEE

Student Member	3500 INR
Academic Delegates	4500 INR
Industrial Delegates	5000 INR
Foreign Delegates	54.39 USD
Attendee & Presentee only	1000 INR

IMPORTANT LINKS AND DATES

Paper Submission Open:
22nd December 2021

Paper Submission Deadline:
15th April 2022

Notifications of Acceptance:
Notification N1: 28th March 2022

Notification N2: 25th April 2022

Notification N3: 10th May 2022

Camera Ready Paper Submission Deadline:

16th May 2022

Conference Dates:
21st & 22nd May 2022

Paper Submission Link:

<https://easychair.org/conferences/?conf=icissi2022>

Conference Website Link:

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ABOUT S.I.E.T

Started with just one institute Shambhunath Institute of Engineering and Technology (SIET) in 2004, now SIET has been transformed into a group of institutions known as Shambhunath Group of Institutions (Shambhunath Institute of Engineering and Technology (SIET), Shambhunath Institute of Management (SIM), Shambhunath Institute of Law (SIL), Shambhunath Institute of Pharmacy (B.Pharm), Shambhunath College of Education (SCE), Shambhunath Industrial Training Institute (SITI), Shambhunath BTC and Shambhunath Hospital). Under the aegis of Shambhunath Group of Institutions many institutes are flourishing. The journey of SGI started with its first institute i.e. Shambhunath Institute of Engineering and Technology, which is approved by AICTE and affiliated to Dr. A. P. J. Abdul Kalam Technical University, (formerly known as UPTU) Lucknow. The success of Shambhunath Institute of Engineering and Technology (SIET; Code-162) which started with just four disciplines i.e. Electronics and Communication (EC), Computer Science (CS), Electrical Engineering (EE) & Information Technology (IT) with an intake of 60 students in each in the year 2004. In 2006, another branch of Engineering viz. Mechanical Engineering was introduced. The intake of CS and EC was increased by 120 in each branch in the next year i.e., 2007. The institute got approval for evening shift for EC and CS with an intake of 60 students in each in 2010. Approval for evening shift with an intake of 60 students in each of EE and M.E. was accorded to SIET in 2011. In 2012, another branch of Engineering - Civil Engineering was started with an intake of 120 students. Another Highlight of the year was starting M. Tech. courses with VLSI Design (EC) & Production Engineering (ME) with an intake of 24 students. SIET got approval to increase in the intake of ME from 120 to 180 and one more branch of Electrical and Electronics Engineering (EN) was introduced in the year 2013 with an intake of 60 students.