



Apr 19–21, 2024 // Dallas, Texas, United States

## CALL FOR PAPERS

The 17th IEEE Dallas Circuits and Systems (DCAS) conference will be held from April 19 - April 21, 2024. The conference will be held as a hybrid event at the University of Texas at Dallas (UTD). The first day of the conference, April 19th, will be in virtual. For those who wish to attend DCAS 2024 in-person, the on-site events will be held at UTD during Apr 20-21, 2024. This conference is co-sponsored by the IEEE Circuits and Systems (CAS) Society, the IEEE Solid-State Circuits Society, the IEEE Electron Devices Society, UT Dallas and UNT will feature a comprehensive program of technical papers, poster sessions, keynote speakers and workshops. The IEEE DCAS 2024 conference contributions will focus on the following eight tracks.

- **VLSI Design & Hardware security**
- **Sensors & Embedded systems**
- **Emerging Device and Circuit Technologies**
- **Circuits and Systems for Machine Learning**
- **Analog and mixed-signal circuits and systems**
- **RF circuits and systems**
- **Power systems, power electronics and power management integrated circuits**
- **Biomedical circuits and systems**

In addition to these tracks, the conference will also encourage submissions in areas including but not limited to the following.

- VLSI design and methodologies
- Analog and RF mixed signal design and ICs
- Synthesis, DFT and Verification Methodologies
- Design for Manufacturability and Yield
- Hardware-Software Co-Design
- Device-Circuit Co-Design
- Power Systems and Power Electronics
- Computer Aided Design
- Low-Power Design
- Temperature-Aware Design
- Devices and Modeling
- Communications
- Nanoscale Computing and Nano-Electronics
- Hardware Security
- High Performance Computing
- Quantum Computing and Spintronics
- Multi-core and Parallel Architectures
- Networks-on-chip
- Embedded Systems
- Sensors and Sensor Arrays
- Signals and Systems
- Reconfigurable Systems
- Education in Circuits and Systems
- Internet of Things
- Cyber-Physical Systems
- Artificial Intelligence Circuits and Architectures
- Packaging
- Emerging Technologies
- Big Data Processing
- Compressive Sensing
- Cognitive Computing and Deep Learning
- Smart Systems for Automotive
- Personalized Healthcare Systems
- Sustainable Computing and Systems
- Innovative Neurotechnology
- Energy-Aware Systems and Services
- Power Management, Energy Harvesting, and Wireless
- Power 5G and Multi-Gigabit Optoelectronics Comm
- Audio and Video Processing

### Accepted Papers

- Accepted regular and short papers will be archived in IEEE Xplore.
- Each published paper requires a full conference registration.
- All papers must be presented by an author or coauthor of that paper at the conference.
- Grants will be awarded to students of selected papers to offset registration costs.

### Submission Formats

- Regular papers: 4-6 pages
- Short papers: 2 pages
- Posters: 1 page





# THE UNIVERSITY OF TEXAS AT DALLAS

Paper submission deadline

Jan 13, 2024

Author notification

Feb 24, 2024

Final Paper Submission Date

Mar 17, 2024

## DCAS 2024 CONTACT INFORMATION

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[www.DCAS2024.com](http://www.DCAS2024.com)

